



Media Contact:

Rick Bause
IBM Media Relations
845-892-5463
rbause@us.ibm.com

POWER.ORG MEMBER RAPPORT INCORPORATED PREVIEWS NEW ENERGY EFFICIENT CHIP DESIGN

SAN JOSE, California, April 4, 2006 . . . At the Embedded Systems Conference here today, Power.org member Rapport Incorporated and IBM previewed a breakthrough energy-efficient processor design, the Kilocore1025, which will feature 1,024 eight-bit processing elements together with a PowerPC™ core on a single, low-cost chip.

Rapport and IBM are collaborating to utilize Power Architecture technology to provide high-performance processing with extremely low energy consumption. The result is expected to be one of the most energy-efficient processor designs of its kind.

Based on Rapport's Kilocore™ technology and IBM's Power Architecture™ technology, Rapport's next-generation Kilocore1025 chip will process information at higher speeds and orders of magnitude lower power than existing processors.

Rapport's current offering, the KC256, features 256 processing elements, provides more than 25 gigabyte operations/second at well under a single watt of power, and is available with tools and a development platform.

Kilocore-based processors address the shortcomings of conventional chips by putting hundreds or thousands of parallel processing elements together on small chips. These chips feature the most advanced, dynamic architecture available today in working silicon and can be dynamically reconfigured for compute-intensive applications, including mobile gaming, homeland security, server components, image processing, consumer electronics and suitcase supercomputing.

For example, with the Kilocore1025, a user will be able to view streaming live and high-definition video on a low-power, mobile device at five to 10 times the speed of existing processors.

The IBM and Rapport collaboration is expected to bring disruptive solutions to the low-power computing marketplace. Rapport, an emerging Silicon Valley-based startup, will be contributing innovative software solutions as well as its unique Kilocore technology, and IBM will be providing world-class engineering services, foundry and ASIC technologies.

"IBM's openness and flexibility in working with Rapport and recognizing the unrivaled value of our Kilocore technology has been tremendous," said Frank Sinton, Rapport's President. "We couldn't have built this relationship without Power.org. Combining the outstanding Power Architecture with Kilocore will open up Power's extensive application base and developer community to the world of handheld, mobile and in-the-field computing."

"Through collaboration, Power.org is creating unprecedented opportunities for its members," said Nigel Beck, vice president, Technology Marketing, IBM Technology Collaboration Solutions, and Chairman of Power.org. "New collaborative efforts with Power.org members like Rapport will continue to deliver innovations in processor design."

Members expanding Power Architecture ecosystem

Other Power.org members developing technology on open specifications and expanding the Power Architecture ecosystem with new technologies include:

IBM, a founder-level member of Power.org, announced new low-power extensions to its PowerPC 970MP offering. These newest offerings complement IBM's low-power 970FX offering and target clients whose applications demand higher performance but are limited by power constraints. Featuring sub 25-watt typical power for dual 64-bit processors with dual single instruction multiple data (SIMD) units, the offerings remove the power barrier for a broad set of compute-intensive applications, such as medical imaging. IBM also announced power management features for the CPC945 bridge chip. Supporting single, dual and quad processor applications, the CPC945 now offers power management options enabling total power as low as 12 watts. These power efficient offerings provide clients with great flexibility in their system designs by reducing the trade-off of performance versus power across a broader set of applications.

Teak Technologies, a venture capital-backed company which chairs the Power.org technical subcommittee on high-speed packet interconnects, has developed a deterministic packet switching solution that provides reliable service guarantees while managing congestion and maximizing bandwidth. Teak's products leverage Power Architecture technology and stand to enhance the BladeCenter ecosystem, including the CellBroadband Engine-based blade infrastructure.

Thales Computers, a Power.org member, is committed to building systems for severe environmental conditions using the low-power 970FX offering.

About Power.org

Power.org is a community of companies driving innovation around Power Architecture technology. Comprised of leaders in the technology field, Power.org is a framework through which its members engage in collaborative innovation on Power Architecture technology, with the mission of optimizing interoperability and accelerating innovation to drive increased adoption of this technology around the world. Power.org currently has more than 40 members, including Founder members Cadence Design Systems, Chartered Semiconductor Manufacturing, Freescale Semiconductor, IBM, Jabil Circuit, Novell, P.A. Semi, Red Hat, Synopsys, and Thales. For more details on Power.org, visit the website www.power.org